

## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

### Title of Invention

METHOD AND MANUFACTURE OF THIN SILICON ON  
INSULATOR (SOI) WITH RECESSED CHANNEL AND DEVICES  
MANUFACTURED THEREBY

Application Number :

Confirmation Number:

First Named Applicant: Huilong Zhu

Attorney Docket Number: FIS920030303US1

Art Unit:

Examiner:

Search string: ( 6465311 or 6391720 or 6225173 or 4616400 or 5814544 ).pn

### US Patent Documents

**Note: Applicant is not required to submit a paper copy of cited US Patent Documents**

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	6465311	2002-10-15	Shenoy			
	2	6391720	2002-05-21	Sneelal, et al.			
	3	6225173	2001-05-01	Yu			
	4	4616400	1986-10-14	Macksey, et al.			
	5	5814544	1998-09-29	Huang			

### Signature

Examiner Name	Date